

PTO/SB/08a Substitute for Form 1449A/PTO				Application Number	Not Yet Assigned 10/535273
INFORMATION DISCLOSURE STATEMENT BY APPLICANT <i>(Use as many sheets as necessary)</i>				Filing Date	Herewith
				First Named Inventor	Otomo et al.
				Art Unit	Not Yet Assigned 2816
				Examiner Name	Not Yet Assigned Khareem Almo
				Sheet 1 of 1	Attorney Docket

U.S. PATENT DOCUMENTS					
Examiner Initials*	Cite No. ¹	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number-Kind Code ²			
KA		US-6,225,831 B1	05/01/2001	Dalmia et al.	
KA		US-6,421,404 B1	07/16/2002	Nakamura	
KA		US-2003/0142774 A1	07/31/2003	Takasoh et al.	

FOREIGN PATENT DOCUMENTS					
Examiner Initials*	Cite No. ¹	Foreign Patent Document	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Country Code ³ -Number ⁴ -Kind Code ⁵			
KA		JP2000077990	03/14/2000	Victor Co. of Japan Ltd.	X
KA		JP2001144592	05/25/2001	Fujitsu Ltd.	X
KA		JP2002171160	06/14/2002	Nec Eng Ltd.	X

NON PATENT LITERATURE DOCUMENTS					
Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.			
KA		NAKAMURA, KAZUYUKI; FUKAISHI, MUNEO; ABIKO, HITOSHI; MATSUMOTO, AKIRA and YOTSUYANAGI, MICHIO, A 6 Gbps CMOS Phase Detecting DEMUX Module Using Half-Frequency Clock, IEEE, 1998			
KA		SAVOJ, JAFAR and RAZAVI, BEHZAD, A 10-Gb/s CMOS Clock and Data Recovery Circuit with a Half-Rate Linear Phase Detector, IEEE Journal of Solid-State Circuits, Vol. 36, No. 5, May 2001, pages 781-788.			
KA		OHTOMO, YUSUKE; KAWAMURA, TOMOAKI; NISHIMURA, KAZUYOSHI, NOGAWA, MASAFUMI; KOIZUMI, HIROSHI and TOGASHI, MINOURU, A 12.5 Gb/s CMOS BER Test Using A Jitter-Tolerant Parallel CDR, IEEE International Solid-State Circuits Conference, ISSCC Digest of Technical Papers, Feb. 2004, pages 174-175.			

Examiner Signature	/Khareem Almo/	Date Considered	07/20/2006
--------------------	----------------	-----------------	------------

*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

¹Applicant's unique citation designation number (optional). ² See Kind Codes of USPTO Patent Documents at www.uspto.gov or MPEP 901.04. ³ Enter Office that issued the document, by the two-letter code (WIPO Standard ST.3). ⁴ For Japanese patent documents, the indication of the year of the reign of the Emperor must precede the serial number of the patent document. ⁵ Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST.16 if possible. ⁶ Applicant is to place a check mark here if English language Translation is attached.